

LCD Controller Development Environment

- ❑ Available software and drivers from Epson*
 - API (Application Programming Interface)
 - Win32 Utilities (CFG, PLAY) (evaluation purposes)
 - Driver for Linux, WinCE with Source Code
 - Demo software
- ❑ 3rd party GUI tools available from multiple vendors *
***availability differs device to device**
- ❑ Evaluation board with target LCDC (Display panel to be added by user)
- ❑ USB board for link between evaluation board and PC
- ❑ USB driver for Windows

Establishes USB link between PC and LCDC Eva board

```
C:\Documents and Settings\fchui\My Documents\My Program\Demo Product\S1D13513_demo_kit_...
CSCL:MGE 0 Regs: 0x44 0xff 0xff 0xdf 0xaa 0x3a
CSCL:MGE 1 Regs: 0x40 0x55 0x00 0x50 0xbb 0xbb
CSCL:MGE 2 Regs: 0x40 0x55 0x00 0x50 0xcc 0xcc
CSCL:MGE 3 Regs: 0x40 0x55 0x00 0x50 0xdd 0xdd
CSCL HW revision 49
=help

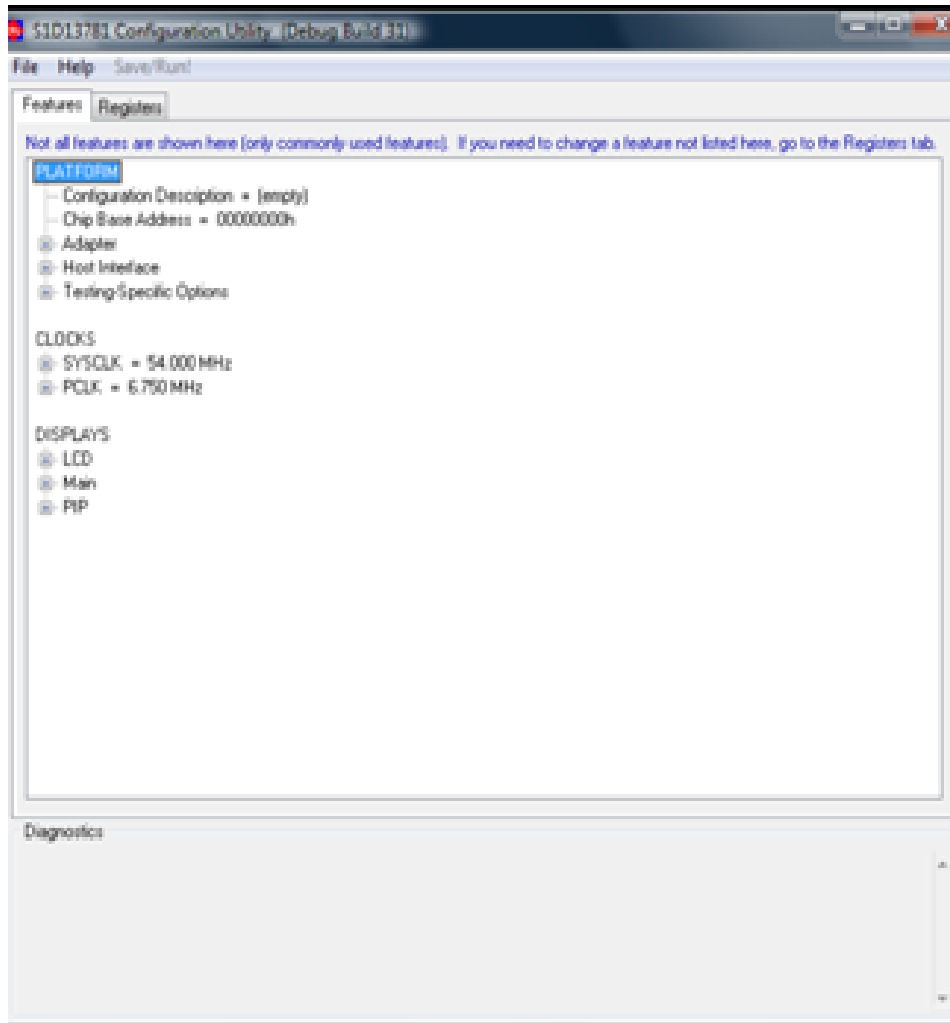
BASIC PLAY COMMANDS          For more details on each command, type: <command> ?
-----
INIT [NOflags|ONLYflags]      - Initializes LCD controller
I[I|D|R|x|E|A|x] [addr [data ..]] - Initializes I2C bus and devices
M                               - Shows current Mode info
MM                              - Shows memory map layout
SHOW [MAIN|PIP1|PIP2|MEM] [...] - Shows a test pattern to screen
SET [token [expression]]      - Sets register, feature, $variable
R[8|16|32] [addr1 addr2 ..]    - Reads from memory
W[8|16|32] [addr [data1 data2 ..]] - Writes to memory
F[8|16|32] start endlen data1 [data2 ..] - Fills memory
D[8|16|32] [start [endlen]]    - Dumps memory to screen
S[8|16|32] start endlen data1 [data2 ..] - Searches memory for a match
X[A|B][8|16|32] [index [data]] - Register Get/Set
XF[8|16|32] index cycle data1 [data2 ..] - Register Fill
XR[8|16|32] index [cycle]      - Register Re-Read
XFB[8|16|32] index cycle data1 [data2 ..] - Register Fill in burst
XRB[8|16|32] index [cycle]     - Register Re-Read in burst
q                               - Quits PLAY
? [regindex|searchtext]       - Help, or use to search chip info

HELP 2=ADVANCED,3=SCRIPT,4=LANGUAGE1,5=LANGUAGE2,6=KEYS,ESC=EXIT [2]>
```

Features

- Load LCDC registers based on Configuration settings
- Access utility commands to test display
- Access utility commands to review register and memory contents and status
- Make change and debug if necessary

Intuitive interface allows easy LCDC configuration



Features

- Set up LCD display panel configuration
- Set up Memory configuration
- Set up Clock configuration
- Set up CPU interface configuration
- Set up other features
 - host interface
 - clock
 - display
 - PIP

API (Application Programming Interface)

- ❑ Sample C codes for fast implementation of chip functions and commands .
 - ❑ BLT – seBlt Data
 - ❑ Clock
 - ❑ Main Layer Functions
 - ❑ Hardware Control Layer
 - ❑ PIP - setRotation, AlphaBlend, Transparency
- ❑ Ready to be integrated and compiled with user program in development environment .
- ❑ Contains the top-level project files
- ❑ The structure of the 13781 API folders is as follows:

DOCUMENTATION contains 13781 API documentation/notes files

HCL Contains the Hardware Control Layer (register and memory reads and writes)

INCLUDE Contains the 13771 API header files (interface)

OS Contains the OS-specific coding layer

PCIBRIDGE Contains the Epson PCI Eval Card layer (used by OS)

SAMPLE Contains test/sample code demonstrating the API

DEBUG Contains pre-compiled sample applications

Segger - emWin

An efficient, processor- and LCD controller-independent graphical user interface (GUI) for any application that operates with a graphical LCD.

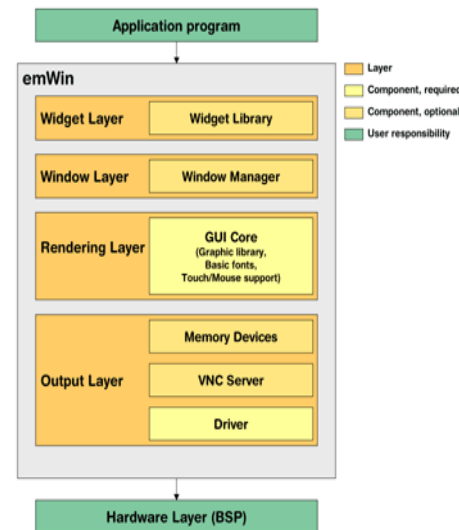
Compatible with single-task and multitask environments, with a proprietary operating system or with any commercial RTOS. emWin is shipped as "C" source code.

It may be adapted to any size physical and virtual display with Epson LCD controller and CPU.



Features

- Any 8/16/32-bit CPU; only an ANSI "C" compiler is required.
- Any (monochrome, grayscale or color) LCD with Epson controller supported
- Any interface supported using configuration macros.
- Display-size configurable.
- Characters and bitmaps may be written at any point on the LCD,
- Routines are optimized for both size and speed.
- Compile time switches allow for different optimizations.
- Virtual display support; the virtual display can be larger than the actual display.



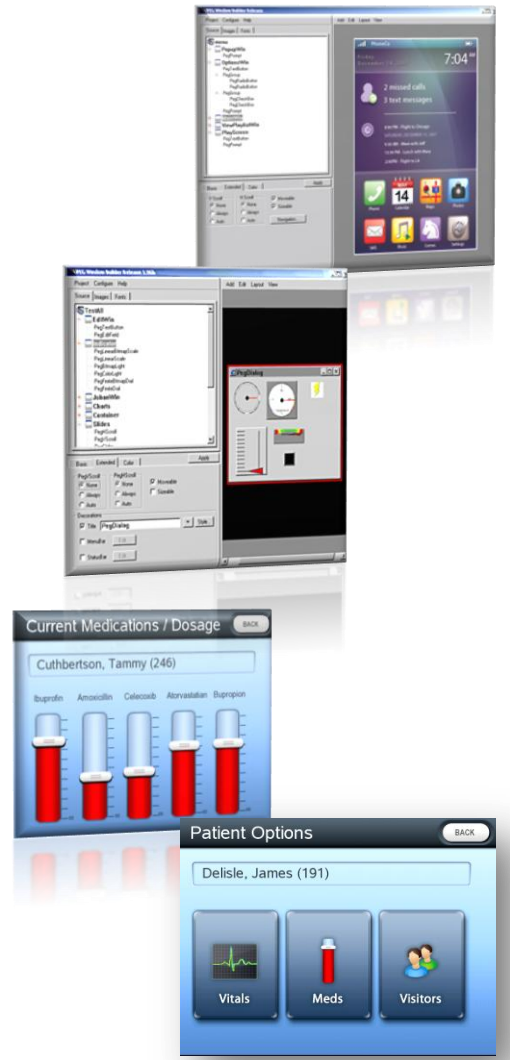
Swell Software – PEG

Portable Embedded GUI and WindowBuilder visual design tool

- PEG Pro™ - for today's most sophisticated graphics and high-resolution displays
- PEG+™ - high performance, small footprint GUI tool for full color LCDs up to 24 bits per pixel (bpp)
- C/PEG™ - extremely efficient; designed for small displays, limited memory requirements & lower color depths

Key Features of PEG GUI software:

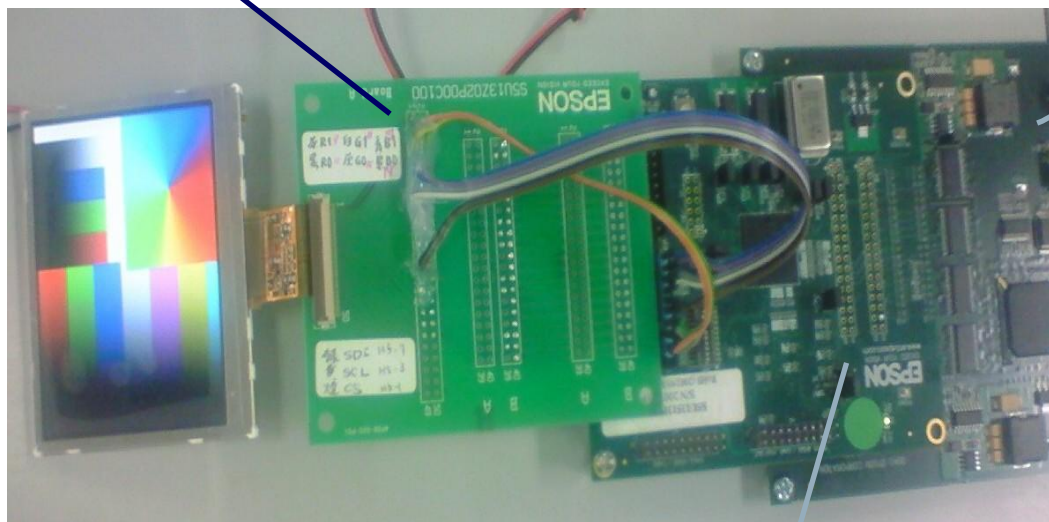
- Small footprint, fast execution speed
- 3 graphics libraries to choose from
- High color depth support, up to 32 bits per pixel
- Multilingual support, including UNICODE and UTF-8
- Includes PEG WindowBuilder desktop prototyping tool
- Touchscreen support available
- Supports the industry's leading processors and graphics controllers
- Runs stand-alone & with more than 20 embedded RTOSs



Evaluation System(S1D13781 example)

S5U13Z02P00C100
Interface board to the
LCD panel

S5U13U00P00C100
USB adaptor board
to the PC.



S5U13781P00C100
Evaluation Board